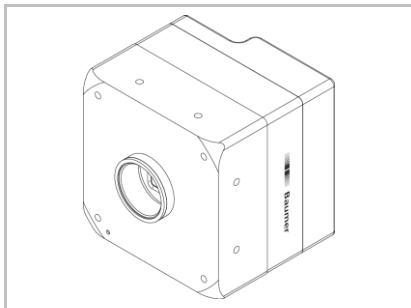
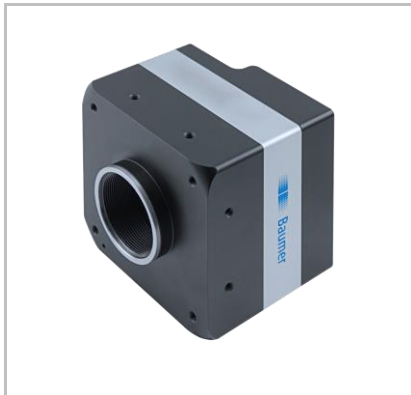
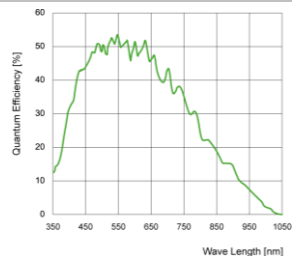


LXG-20M.PS

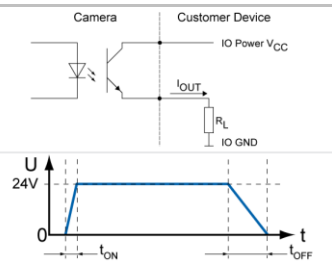
Technical Data

 Art. No.
11174305


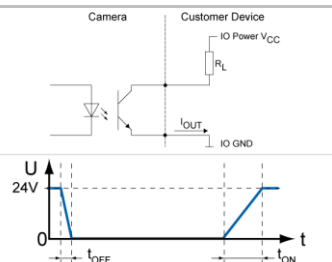
Sensor Graph: Relative Response



Digital Output: High Active



Digital Output: Low Active



Digital Monochrome Matrix Camera, Gigabit Ethernet, Visual Applets for FPGA Image Pre-processing

Sensor Information

Model Name	CMOSIS CMV-2000 V3
Type	2/3" progressive scan CMOS
Shutter	Global
Native Resolution	2048 x 1088 pixels
Scan Area	11.26 mm x 5.98 mm
Pixel Size	5.5 μm x 5.5 μm

Data Quality

@ 20 °C, gain = 1, exposure time = 4 msec

Readout Noise (σ)	0.4 LSB @ 8 bit
Dynamic Range	54.5 dB (typical)

Acquisition Formats

Image Formats	Format	Resolution	Sensor/GigE	t_{readout}
	Full Frame	2048 x 1088	338/56 fps	2.94 msec
Pixel Formats	Mono10 (Sensor), Mono8 / Mono10Packed (GigE)			
Partial Scan	True Partial Scan, Region of Interest (ROI) arbitrary			

Image Pre-Processing

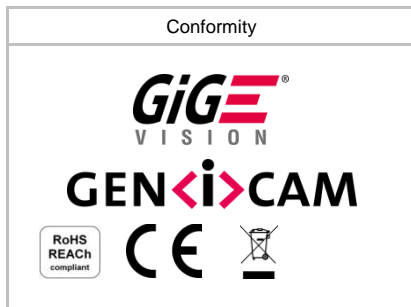
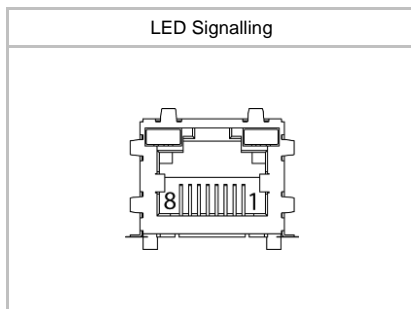
Analog Controls	Exposure Time (30 μsec ... 1 sec Step Size 1 μsec) Gain (0 ... 12 dB), Offset (0 ... 63 LSB 10 bit)
Gamma Correction	NA
LUT	NA
Color Models	Mono
Color Tolerance	Only on Color Cameras
Color Processing	NA
Color Adjustment	NA
Binning	NA
Decimation	NA
Image Flipping	Horizontal
Defect Pixel Correction	via Defect Pixel List with up to 1000 Pixel Coordinates, 10 Lines, 10 Columns

Process Synchronization

Modes	Free Running, Trigger
Free Running	Continuous or Adjustable Acquisition Frame Rate (0.01 ... 19230 Hz)
Trigger Sources	line0, Software, ActionCommand, Timer1Start, eVAOutput1, All (except Timer1Start/eVAOutput1) or Off
Trigger Delay	0 ... 2 sec, Tracking and buffering of up to 256 triggers
Sequencer Characteristics	up to 128 sets of parameters, up to 65536 loop passes, up to 65536 repetitions of sets of parameters, up to 65536 images per trigger event
Sequencer Parameters	Exposure Time, Gain Factor, ROI Offset x, ROI offset y
External Flash Sync	via Exposure Active $t_{\text{delay flash}} \leq 3 \mu\text{sec}$, $t_{\text{duration}} = t_{\text{exposure}} + 18 \mu\text{sec}$

Digital I/Os

Lines	Input: Line 0, Output: Line1, Line 2, Line 3
Circuit Times	Output: $t_{\text{ON}} = \text{typ. } 2 \mu\text{sec}$ $t_{\text{OFF}} = \text{typ. } 30 \mu\text{sec}$
Output Sources	Off, ExposureActive, ReadoutActive, FrameActive, TriggerReady, TriggerOverlapped, TriggerSkipped, Line 0, UserOutput{1,2,3}, Timer{1,2,3}Active, eVAOutput{1,2,3}
Line Debouncer	Low and high signal separately selectable Debouncing Time 0 ... 5 msec, Step Size: 1 μsec



LED Signalling

Camera LED	Green on	Power on, link good
	Green blinking	Power on, no link
	Red on	Error
	Red blinking	Warning
	Yellow	Readout active
RJ45 LEDs	Green on	Link on
	Green blinking	Link activity
	Amber on	GigE speed
	Amber blinking	100 Mb speed

Environmental Data

Storage Temperature	-10 °C bis +70 °C
Operating Temperature	+5°C ... +50°C
Device Temperature	T _{max} = 50 °C @ Measurement Point
Humidity	10 % ... 90 % non-condensing
Conformity	RoHS, REACH, CE

Network Interface Data

Network Interface	Gigabit Ethernet	1000BASE-T	1000 Mbits/sec
	Fast Ethernet	100 BASE-T	100 Mbits/sec
Link Aggregation	According to 802.3ad, static configuration		
Ethernet IP Configuration	Persistent IP, DHCP, LLA		
Packet Size	576 .. 9000 Byte, Jumbo frames supported		

GigE Vision® Features (in compliance with GigE Vision® 1.2)

Events Transmission via Asynchronous Message Channel	GigEVisionError, HeartbeatTimeOut, EventLost, EventDiscarded, Line{0,1,2,3}RisingEdge, Line{0,1,2,3}FallingEdge, Action1, ExposureStart, ExposureEnd, FrameStart, FrameEnd, TriggerReady, TriggerOverlapped, TriggerSkipped, Timer{1,2,3}End
Frame Counter	up to 2 ³²
Lost Frame Counter	up to 2 ²⁴ - 1, counts discarded images when FIFO is full
Payload Size	4 ... 2.228.436 Byte
Transmission Delay	0 .. 2 ³² -1 Ticks (1 Tick = 8 nsec)
Timestamp	64 bit
Packet Delay	0 .. 2 ³² -1 Ticks (1 Tick = 8 nsec)
Packet Resend	Resend Buffer: 240 MB (16 Images)

GeniCam™ Features (in compliance with SFNC 2.1.0)

Timer	Timer Selector: Timer 1 ... 3 TimerTriggerSource: Off, Line0, Software, Action1, TriggerSkipped ExposureStart, ExposureEnd, FrameStart, FrameEnd, TimerDelay: 0 µsec ... 2 sec, Step Size: 1 µsec TimerDuration: 10 µsec ... 2 sec, Step Size: 1 µsec
User Sets	Factory Settings: Default (read only) Freely Programmable: UserSet1, UserSet2, UserSet3 Parameters: any user definable Parameter
Acquisition Abort	Delay up to 69 msec

Vendor Specific Features

DSNU / PRNU (FPN)	Based on offset / gain per column
Correction	
High Dynamic Range (HDR)	Piecewise linear response, up to 90 dB
Burst Mode	NA
eVA Applet Enable	On / Off
eVA A. Overlapped Images	Number of images than can be processed in parallel in eVA
Temperature measurement	Internal sensor temperature range: 0 °C .. +85 °C, accuracy: ±1,0 °C

Embedded Visual Applets

eVA_Info	Version, platform and applet information
ImgIn	Maximum resolution: 2048 x 1088 Maximum frame rate at full resolution: 337 fps Interface: 12 bit, parallelism 8, 96 MHz Buffered within camera via FIFO, up to 16 lines Max bandwidth: 2048 x 1088, 337 fps, 12 bit (1075 MB/s)
ImgOut	Maximum resolution: 2048 x 7168 (8 bit), x 3584 (10 bit) Maximum frame rate via GigE: 29 fps (full frame) Interface: 12 bit, parallelism 4, 96 MHz Buffered within camera via memory, up to 16 images Please note: images are transferred as 8 or 10 bit to PC
MetaDataOut	Defines properties of images transferred to ImgOut.
ChunkOut	NA
Inputs	In{0,1,2,3,4} Input 0: Connected to IN1 (line0) Input 1: Connected to RS485_IN_A Input 2: Connected to RS485_IN_B
Outputs	Out{0,1,2,3} Output 0: Connected to eVAOutput1 Output 1: Connected to eVAOutput2 Output 2: Connected to eVAOutput3 Output 3: Connected to RS485_OUT_A
FPGA	Xilinx Spartan 6 XC6SLX150-3CSG484C Please note: the FPGA is shared with the camera design
Dynamic Memory	2 ports, 64 bit
Persistent Memory	NA
LicenseStatus	Provides information of installed licenses

Factory Settings after Start-Up

Operation Mode	Free running, overlapped mode
Analog Controls	Exposure Time: 4 msec, Gain: 0 dB, Offset: 0
Pixel Format	Mono8
Partial Scan	Off
Acquisition Frame Rate	Off
Timer	Off
Transmission Delay	Off
Defect Pixel Correction	On
FPN Correction	On
eVA Applet Enable	Off
Digital Input	Line0, invert = false, trigger source = All
Digital Output	Line1/2/3, invert = false, line source = Off